

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#2 | IDS
6-13-02
Examiner

Applicants : Federico Pio et al.
 Filed : March 4, 2002
 For : IMPROVED FIELD-EFFECT TRANSISTOR AND
 CORRESPONDING MANUFACTURING METHOD
 Docket No. : 856063.616D1
 Date : March 4, 2002

Box Patent Application
 Commissioner for Patents
 Washington, DC 20231

J1002 U.S. PTO
10/090978
03/04/02


INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

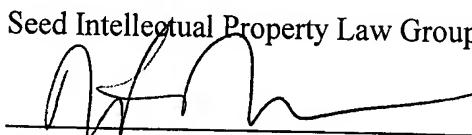
In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached Form PTO-1449. This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior Application No. 09/442,834, filed November 18, 1999. The references listed on the attached Form PTO-1449 were submitted to and/or cited by the Patent and Trademark Office in this prior application and, therefore, are not required to be provided in this application. If the Examiner wishes, copies will be provided upon request. As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

Federico Pio et al.

Seed Intellectual Property Law Group PLLC



Robert Iannucci

Registration No. 33,514